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Application/Control Number: 09/499,619
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1. (Amended) A display device comprising:

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a substrate;
a plurality of first signal lines formed over said substrate;
a plurality of second signal lines extending across said first signal lines over said substrate;
a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one first thin film transistor wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality of first signal lines and an impurity region of said first thin film transistor is electrically connected to one of said plurality of second signal lines;
a smoothing film comprising an organic resin formed over said switching elements;
a plurality of pixel electrodes formed over said smoothing film and electrically connected to said switching elements through contact holes formed in said smoothing film; and
a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and
a second driver circuit electrically connected to said plurality of first signal lines, wherein said second driver circuit comprises second thin film transistors formed over said substrate.

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2. The display device according to claim 1 wherein said pixel electrodes comprise indium tin oxide.

CANCEL CLAIMS 3-4

3. (Amended) The display device according to claim 1 wherein at least one of said first and second thin film transistors has a channel region comprising semi-amorphous silicon.

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4. (Amended) The display device according to claim 1 wherein each of said first and second thin film transistors has a top-gate structure.

5.

The display device according to claim 1 wherein said smoothing film comprises polyimide.

~~6.~~ (Amended) A display device comprising:
a substrate;
a plurality of first signal lines formed over said substrate;
a plurality of second signal lines extending across said first signal lines
over said substrate;
a plurality of switching elements formed at each intersection of said first and

second signal lines, each of said switching elements comprising at least one first thin film transistor, wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality of first signal lines and an impurity region of said first thin film transistor is electrically connected to one of said plurality of second signal lines;

a smoothing film comprising an organic resin formed over said switching elements;

a plurality of pixel electrodes formed over said smoothing film and electrically connected to said switching elements through contact holes formed in said smoothing film; and
a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and

a second driver circuit electrically connected to said plurality of first signal lines, wherein said second driver circuit comprises second thin film transistors formed over said substrate,

wherein said first driver circuit is connected to said substrate through a tape automated bonding process.

~~7.~~ The display device according to claim ~~8~~ wherein said IC chip is mounted on a flexible support.

~~8.~~ The display device according to claim ~~8~~ wherein said pixel electrodes comprise indium tin oxide.

CANCEL CLAIMS 11-12

~~9.~~ (Amended) The display device according to claim ~~8~~ wherein at least one of said first and second thin film transistors has a channel region comprising semi-amorphous silicon.

~~10.~~ (Amended) The display device according to claim ~~8~~ wherein each of said first and second thin film transistors has a top-gate structure.

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11. ~~16.~~ A display device comprising:
a substrate;
a plurality of first signal lines formed over said substrate;
a plurality of second signal lines extending across said first signal
lines over said substrate;
a plurality of switching elements formed at each intersection of said
first and second signal lines, each of said switching elements comprising at least one
thin film transistor;
20 a smoothing film comprising an organic resin formed over said
switching elements;
a plurality of pixel electrodes formed over said smoothing film and
electrically connected to said switching elements through contact holes formed in
said smoothing film; and
25 a driver circuit comprising at least one IC chip for driving said
switching elements,
wherein said IC chip is formed on a flexible support and said flexible
support is connected to said substrate.)

12. ~~16.~~ The display device according to claim ~~11~~ wherein said thin film
transistor has a top-gate structure.

13. ~~16.~~ The display device according to claim ~~15~~ wherein said pixel
electrodes comprise indium tin oxide.

14. ~~16.~~ The display device according to claim ~~16~~ wherein said IC chip is
electrically connected to either one of said first signal lines or said second signal
lines.

15. ~~16.~~ The display device according to claim ~~16~~ wherein said driver circuit
further comprises thin film transistors formed over said substrate.

16. ~~16.~~ The display device according to claim ~~16~~ wherein said thin film
transistor has a channel region comprising semi-amorphous silicon.

17. (Amended) A display device comprising:
a substrate;
a plurality of first signal lines formed over said substrate;
a plurality of second signal lines extending across said first signal lines over said
substrate;
a plurality of switching elements formed at each intersection of said first and
second signal lines, each of said switching elements comprising at least one first thin film
transistor, wherein a gate electrode of said first thin film transistor is electrically connected to
one of said plurality of first signal lines and an impurity region of said first thin film transistor

is electrically connected to one of said plurality of second signal lines;
a smoothing film comprising an organic resin formed over said switching elements;
a plurality of pixel electrodes formed over said smoothing film and electrically connected to said switching elements through contact holes formed in said smoothing film; and
a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and
a second driver circuit electrically connected to said plurality of first signal lines
wherein said second driver circuit comprises second thin film transistors formed over said substrate,

18 22. (Amended) The display device according to claim 21 wherein each of said first and second thin film transistors has a top-gate structure.

19 23. The display device according to claim 24 wherein said pixel electrodes comprise indium tin oxide.

CANCEL CLAIMS 24-25

B7 20 26. (Amended) The display device according to claim 21 wherein at least one of said first and second thin film transistors has a channel region comprising semi-amorphous silicon.

21 27. A display device comprising:
a substrate;
a plurality of first signal lines formed over said substrate;
a plurality of second signal lines extending across said first signal lines over said substrate;
a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one first thin film transistor wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality of first signal lines and an impurity region of said first thin film transistor is electrically connected to one of said plurality of second signal lines;
an organic resin film formed over said switching elements;
a plurality of pixel electrodes formed over said organic resin film and electrically

connected to said switching elements through contact holes formed in said organic resin film;
and

a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and
a second driver circuit electrically connected to said plurality of first signal lines,
wherein said second driver circuit comprises second thin film transistors formed over said substrate.

~~22~~ ²⁴ ~~26~~ The display device according to claim ~~27~~ wherein said organic resin film
comprises polyimide.

~~23~~ ²⁴ ~~26~~ The display device according to claim ~~27~~ ²¹ wherein each of said first and second
thin film transistors has a top-gate structure.

~~24~~ ²⁴ ~~36~~ A display device comprising:
a substrate;
a plurality of first signal lines formed over said substrate;
a plurality of second signal lines extending across said first signal lines over said substrate;
a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one thin film transistor;
an organic resin film formed over said switching elements;
a plurality of pixel electrodes formed over said organic resin film and electrically connected to said switching elements through contact holes formed in said organic resin film;
and
a driver circuit comprising at least one IC chip for driving said switching elements,
wherein said IC chip is formed on a flexible support and said flexible support is connected to said substrate.

~~25~~ ²⁴ ~~36~~ The display device according to claim ~~36~~ wherein said organic resin film

comprises polyimide.

~~24~~ 32. The display device according to claim ~~20~~ wherein said driver circuit includes a first driver circuit including said IC chip and electrically connected to said plurality of second signal lines and a second driver circuit comprising second thin film transistors and electrically connected to said plurality of first signal lines.

~~25~~ 33. A display device comprising:
a substrate;
a plurality of first signal lines formed over said substrate;
a plurality of second signal lines extending across said first lines over said substrate;
a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one first thin film transistor, wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality of first signal lines and an impurity region of said first thin film transistor is electrically connected to one of said plurality of second signal lines;
an organic resin film formed over said switching elements;
a plurality of pixel electrodes formed over said organic resin film and electrically connected to said switching elements through contact holes formed in said organic resin film;
and
a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and
a second driver circuit electrically connected to said plurality of first signal lines wherein said second driver circuit comprises second thin film transistors formed over said substrate.
wherein said IC chip is mounted over said substrate.

~~26~~ 34. The display device according to claim ~~33~~ wherein said organic resin film comprises polyimide.

~~29~~ 35. The display device according to claim ~~35~~ ²⁹ wherein each of said first and second thin film transistors has a top-gate structure.

~~30~~ 36. A display device comprising:
a substrate;
a plurality of first signal lines formed over said substrate;
a plurality of second signal lines extending across said first signal lines over said substrate;
a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one first thin film transistor wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality of first signal lines and an impurity region of said first thin film transistor is electrically connected to one of said plurality of second signal lines;
a smoothing film formed over said switching elements;
a plurality of pixel electrodes formed over said smoothing film and electrically connected to said switching elements through contact holes formed in said smoothing film; and
a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and
a second driver circuit electrically connected to said plurality of first signal lines, wherein said second driver circuit comprises second thin film transistors formed over said substrate.

~~31~~ 37. The display device according to claim ~~36~~ ³⁰ wherein each of said first and second thin film transistors has a top-gate structure.

~~32~~ 38. A display device comprising:
a substrate;
a plurality of first signal lines formed over said substrate;
a plurality of second signal lines extending across said first signal lines over said substrate;
a plurality of switching elements formed at each intersection of said first and

second signal lines, each of said switching elements comprising at least one thin film transistor;
a smoothing film formed over said switching elements;
a plurality of pixel electrodes formed over said smoothing film and electrically connected to said switching elements through contact holes formed in said smoothing film; and
a driver circuit comprising at least one IC chip for driving said switching elements.

wherein said IC chip is formed on a flexible support and said flexible support is connected to said substrate.

33 ~~35~~ ³⁹ The display device according to claim 36 wherein said driver circuit includes a first driver circuit including said IC chip and electrically connected to said plurality of second signal lines and a second driver circuit comprising second thin film transistors and electrically connected to said plurality of first signal lines.

34 ~~37~~ A display device comprising:
a substrate;
a plurality of first signal lines formed over said substrate;
a plurality of second signal lines extending across said first signal lines over said substrate;
a plurality of switching elements formed at each intersection of said first and second signal lines, each of said switching elements comprising at least one first thin film transistor, wherein a gate electrode of said first thin film transistor is electrically connected to one of said plurality of first signal lines and an impurity region of said first thin film transistor is electrically connected to one of said plurality of second signal lines;
a smoothing film formed over said switching elements;
a plurality of pixel electrodes formed over said smoothing film and electrically connected to said switching elements through contact holes formed in said smoothing film; and
a first driver circuit comprising at least one IC chip electrically connected to said plurality of second signal lines; and
a second driver circuit electrically connected to said plurality of first signal lines wherein said second driver circuit comprises second thin film transistors formed over said

substrate,

wherein said IC chip is mounted over said substrate.

~~35~~ 41. The display device according to claim ~~18~~ ¹¹ wherein said driver circuit includes a first driver circuit including said IC chip and electrically connected to said plurality of second signal lines and a second driver circuit comprising second thin film transistors and electrically connected to said plurality of first signal lines.

~~36~~ 42. The display device according to claim 1 wherein each of said first and second driver circuits comprises a shift register circuit.

~~37~~ 43. The display device according to claim ~~8~~ ⁶ wherein each of said first and second driver circuits comprises a shift register circuit.

~~38~~ 44. The display device according to claim ~~24~~ ¹⁷ wherein each of said first and second driver circuits comprises a shift register circuit.

~~39~~ 45. The display device according to claim ~~27~~ ²¹ wherein each of the first and second driver circuits comprises a shift register circuit.

~~40~~ 46. The display device according to claim ~~32~~ ²⁷ wherein each of the first and second driver circuits comprises a shift register circuit.

~~41~~ 47. The display device according to claim ~~36~~ ³⁰ wherein each of the first and second driver circuits comprises a shift register circuit.

~~42~~ 48. The display device according to claim ~~40~~ ³⁴ wherein each of said first and second driver circuits comprises a shift register circuit.

~~43~~ 49. The display device according to claim 1 wherein said first driver circuit comprises a latch circuit.

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~~44~~ 50. The display device according to claim ~~9~~ wherein said first driver circuit comprises a latch circuit.

~~45~~ 51. The display device according to claim ~~24~~ wherein said first driver circuit comprises a latch circuit.

~~46~~ 52. The display device according to claim ~~27~~ wherein said first driver circuit comprises a latch circuit.

~~47~~ 53. The display device according to claim ~~32~~ wherein said first driver circuit comprises a latch circuit.

~~48~~ 54. The display device according to claim ~~36~~ wherein said first driver circuit comprises a latch circuit.

~~49~~ 55. The display device according to claim ~~46~~ wherein said first driver circuit comprises a latch circuit.—

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